1. PC , Epc t, E­b\_to\_a ,MAR
2. R/W = 0, Emsr , E­b\_to\_a ,MBR
3. Embr , E­b\_to\_a , IR
4. Eir , Latch 1 CL1, Latch 2 CL2

1-3) repeat above

1. IR, EIR , E­b\_to\_a , ED1, E­b\_to\_a , MAR, Cmar , R/W = 0, Emsr , E­b\_to\_a , D0 , ED0 , Latch 2 *# make D0 negative then put d0 into latch 2*
2. D0 , ED0 , E­b\_to\_a , MAR, RW=0 , EMSR, Latch 1.